

Figure 1A (Prior Art)

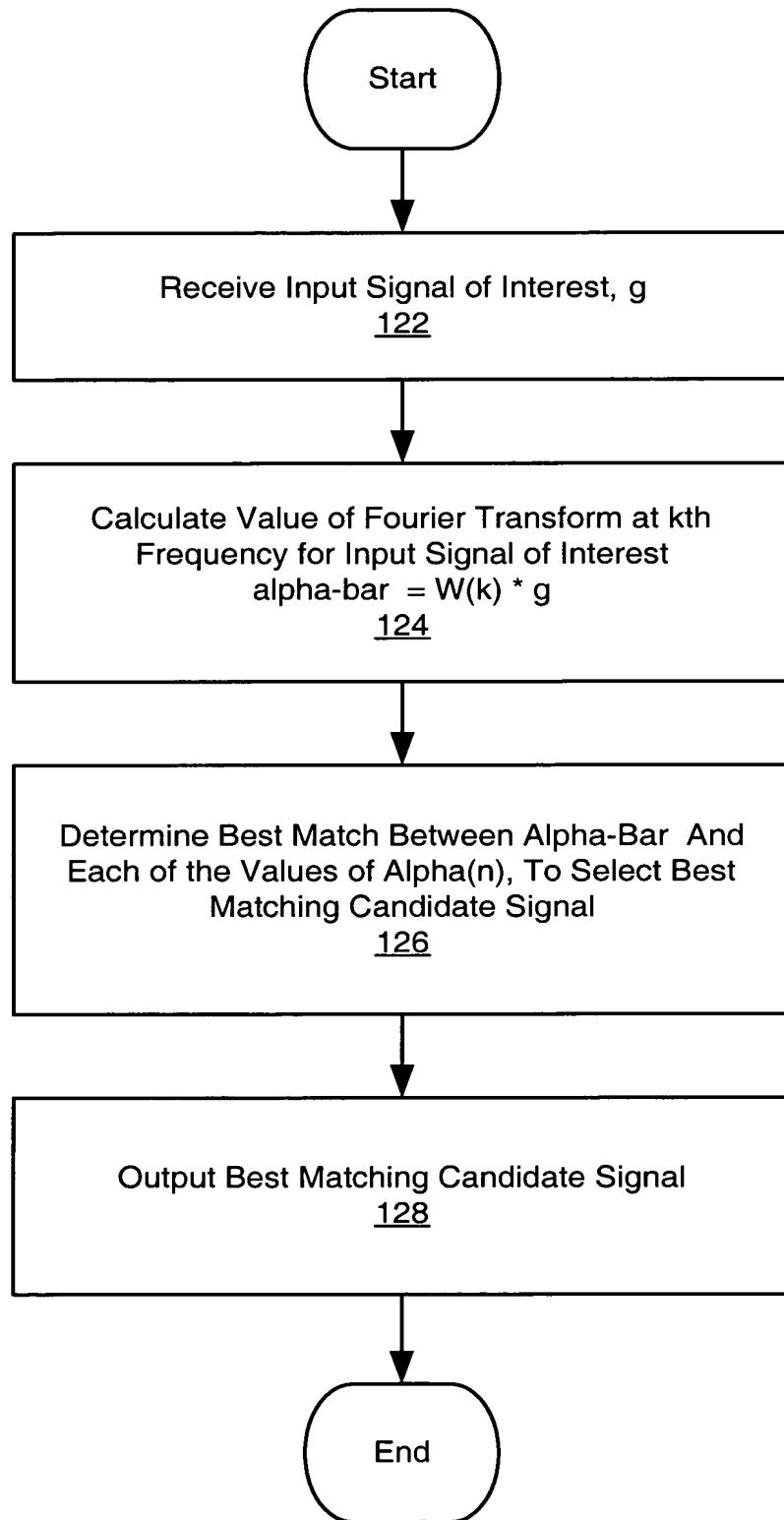
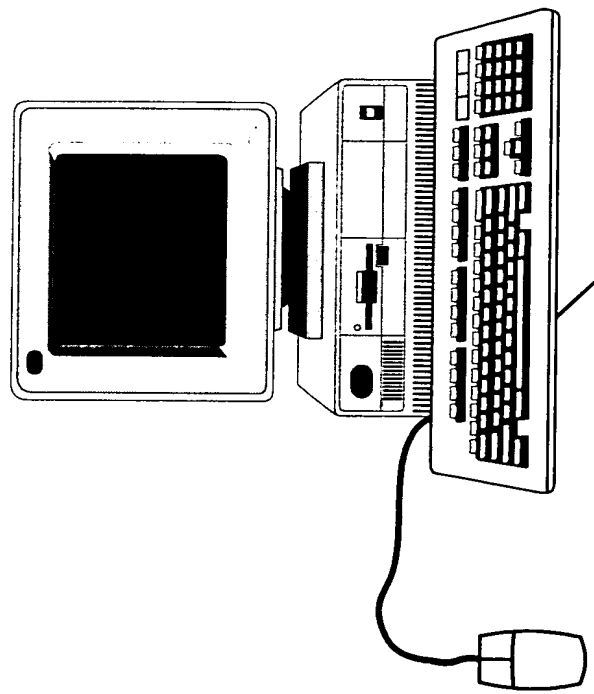


Figure 1B (Prior Art)



Host Computer System
202

Figure 2

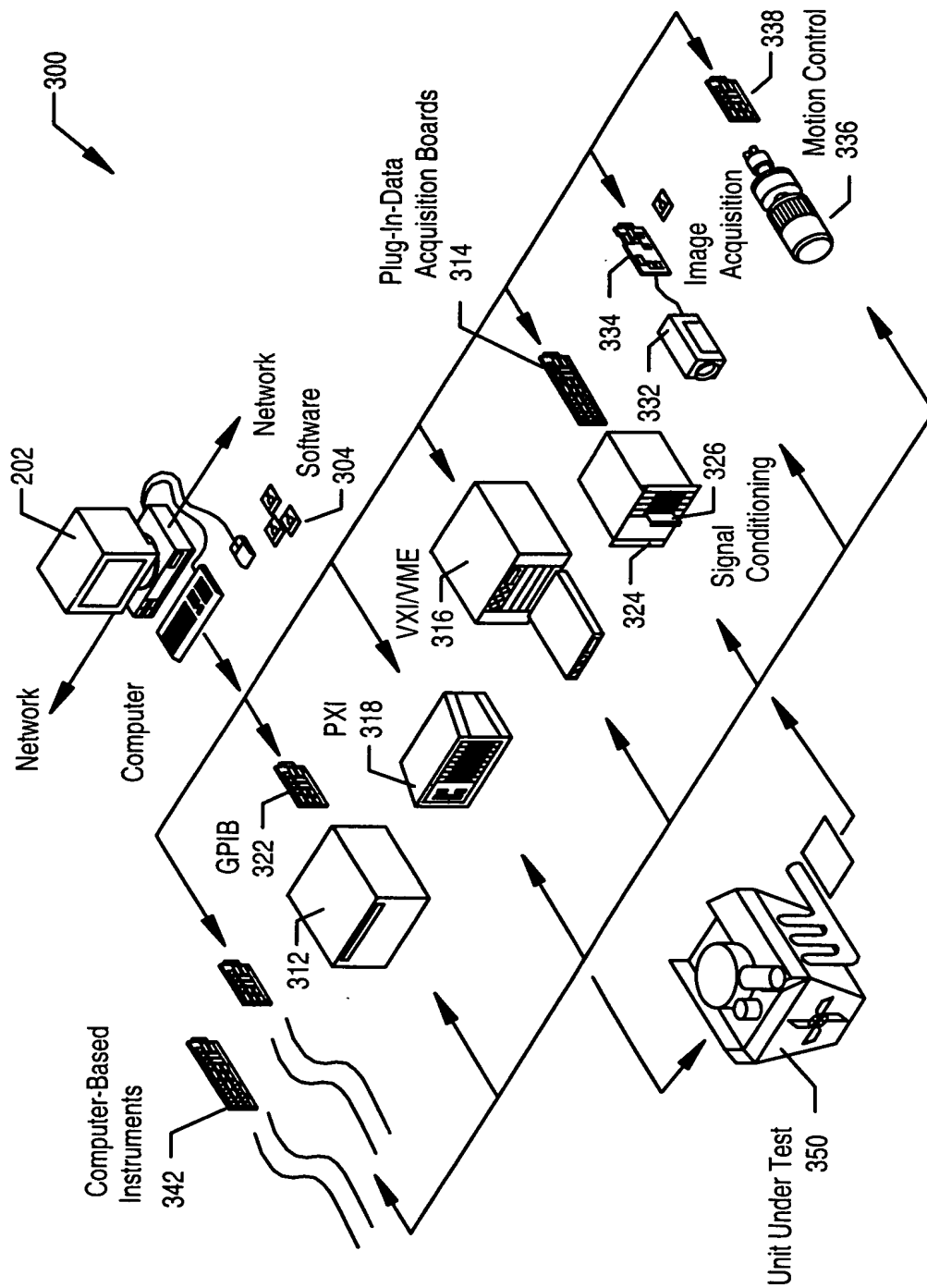


FIG. 3A

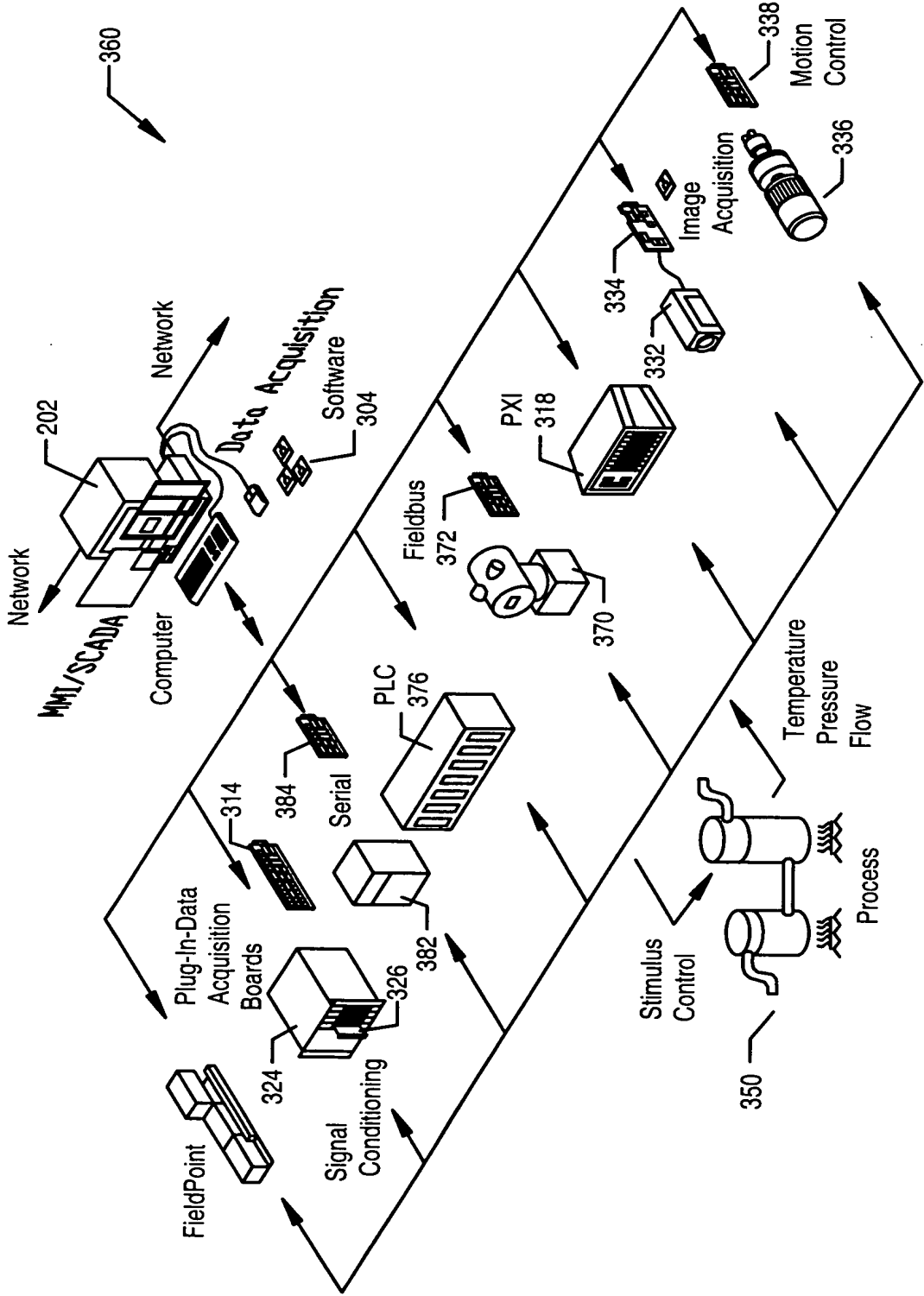


FIG. 3B

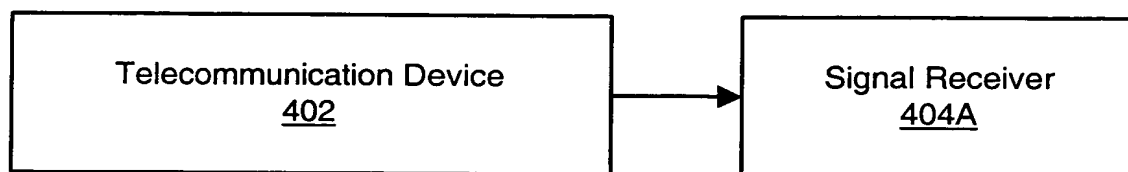


Figure 4A

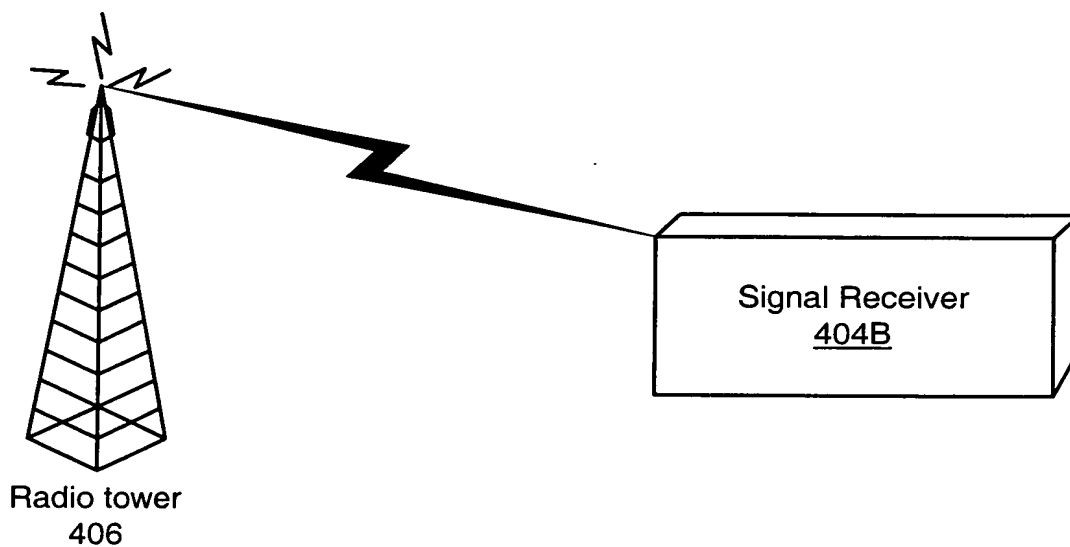


Figure 4B

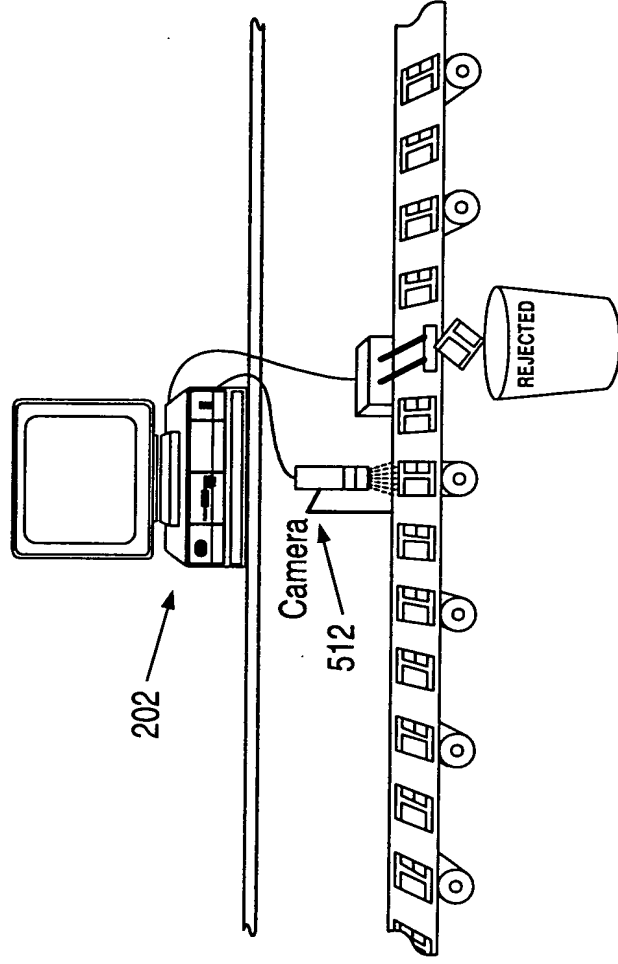


Figure 5

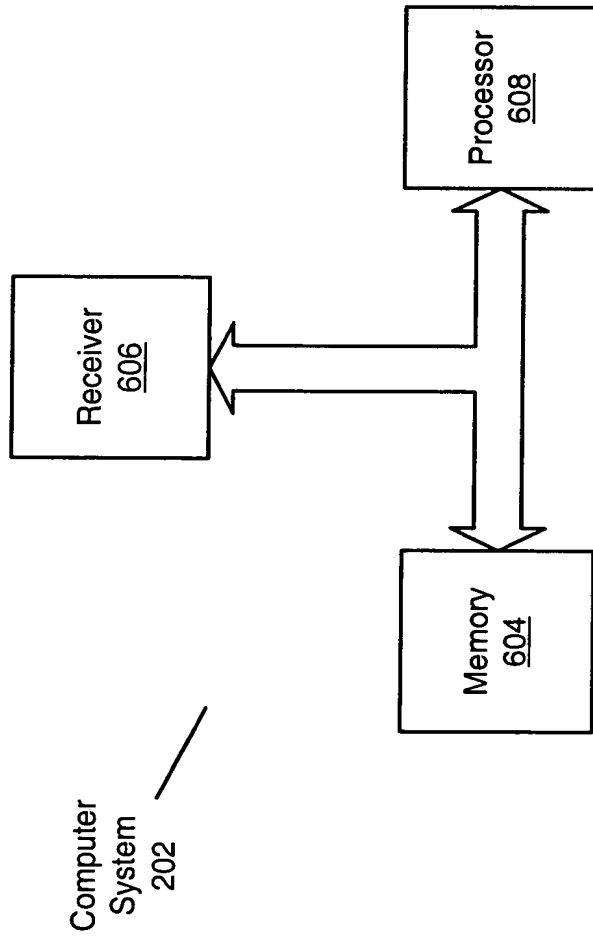


Figure 6

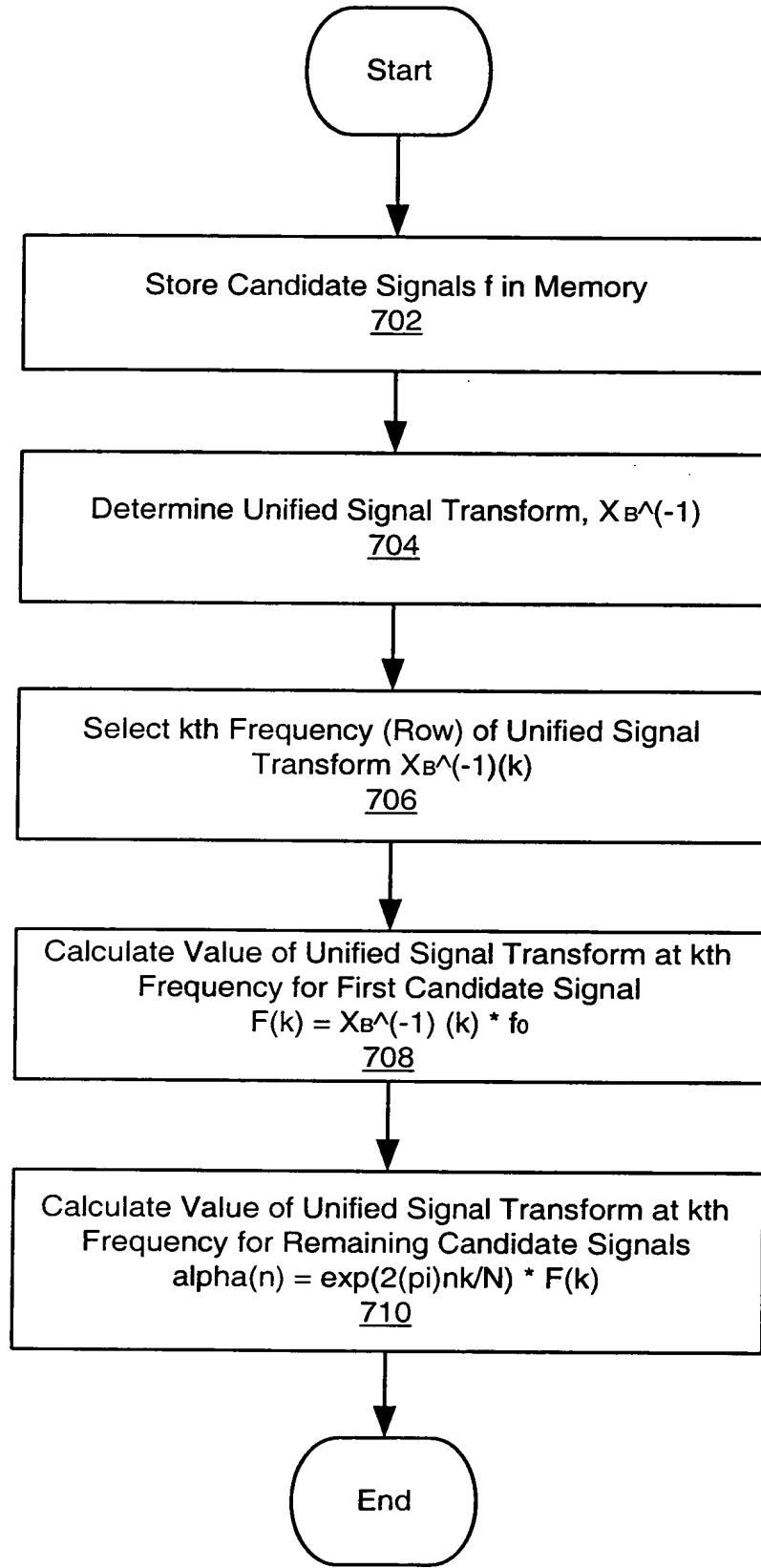


Figure 7A

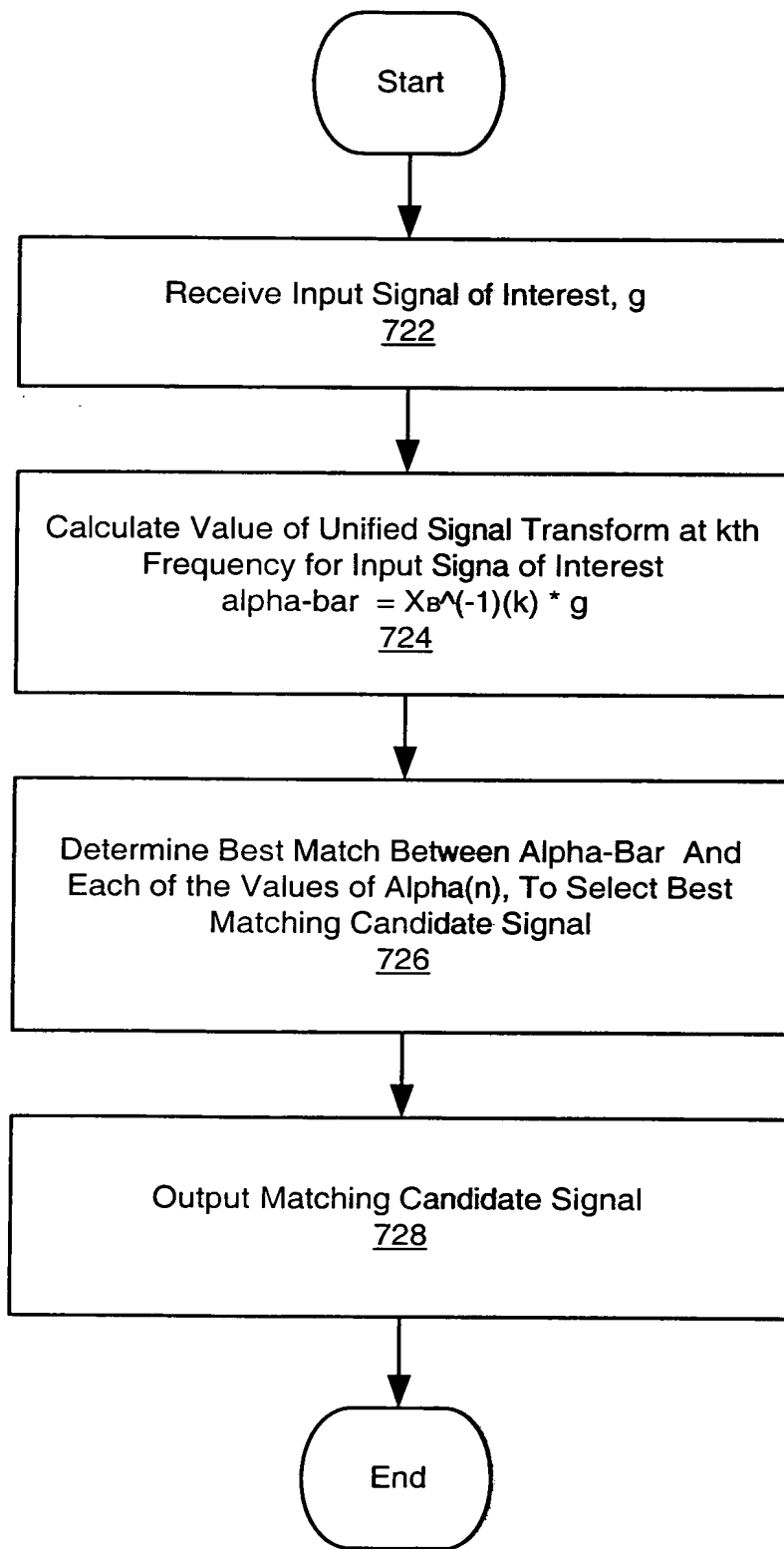


Figure 7B

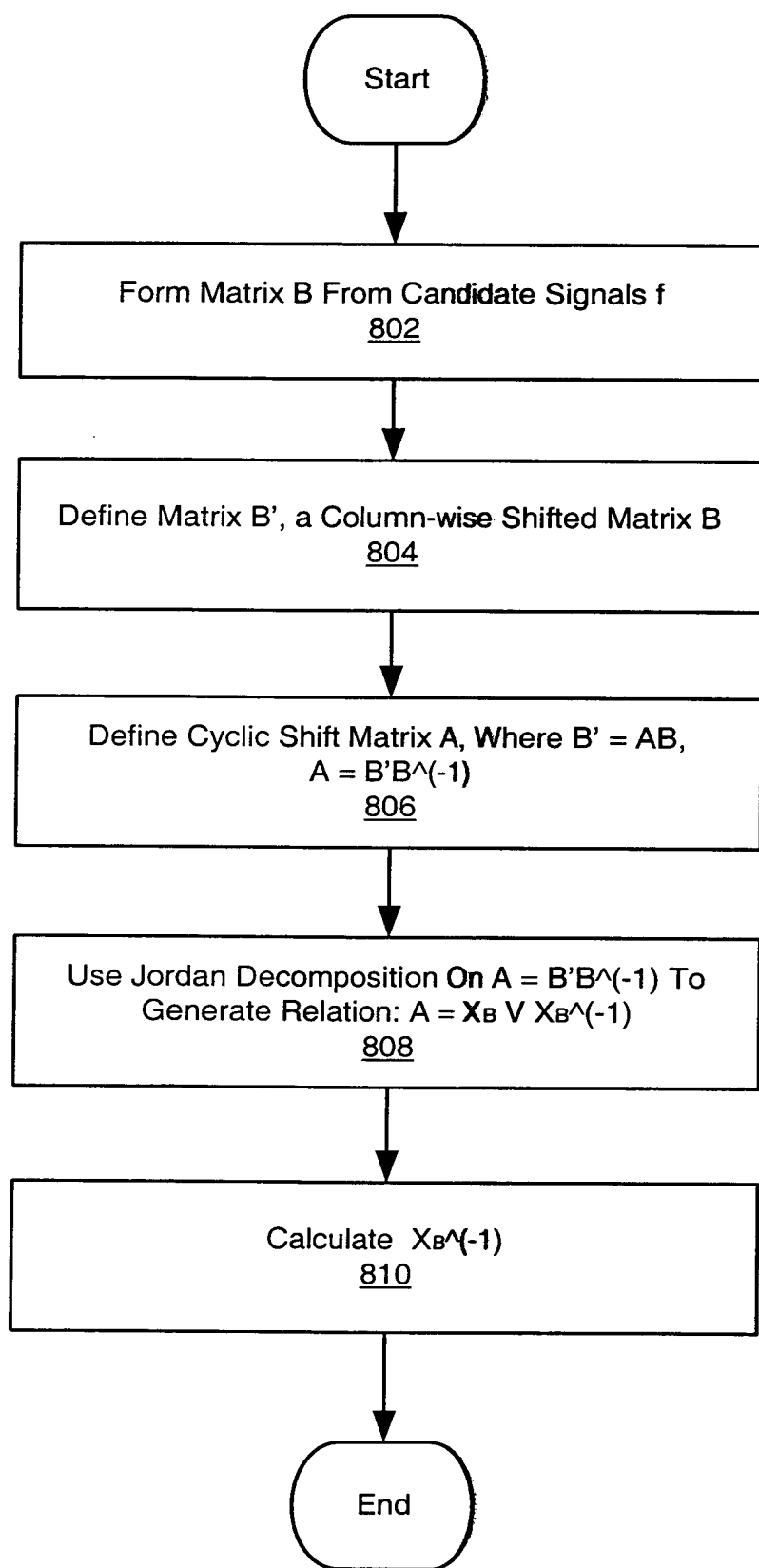
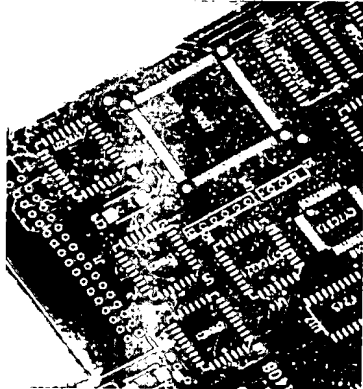
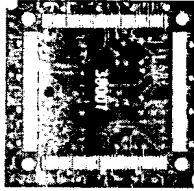


Figure 8



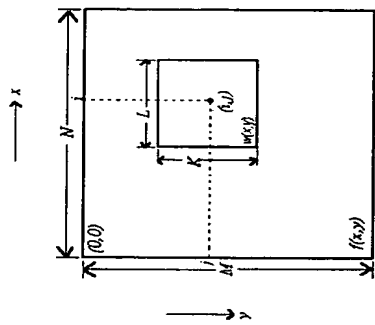
Test Image /
902

Figure 9A



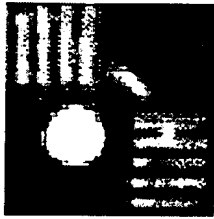
Template
Image, It
904

Figure 9B

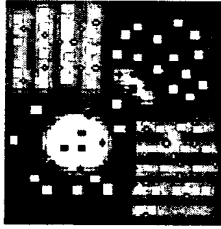


Classic Correlation
906

Figure 9C
(Prior Art)



Reference Pattern
1002



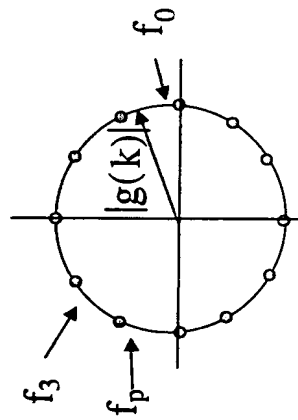
Sampled Pattern
1004



Edges Information
1006

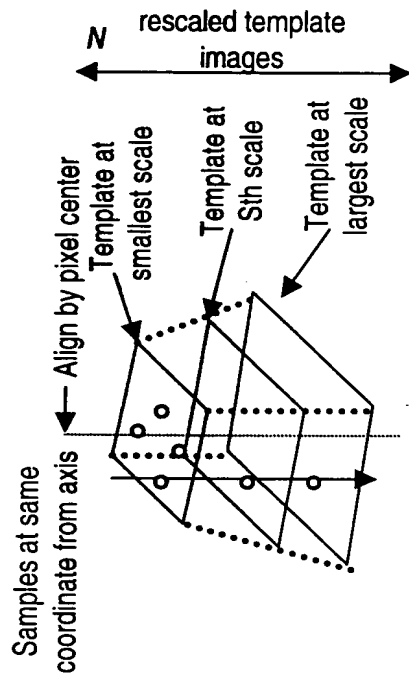
Shift Invariant Pattern Matching

Figure 10



UST Circle
1102

Figure 11A



Pattern Matching Sampling
Strategy
1104

Figure 11B